

1c979 U.S. PTO  
09/874044



708	496	Subclass	ISSUE CLASSIFICATION
Class			

# U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>[Signature]</i> O.A. 15	PATENT DATE
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APPLICATION NO. 09/874044	CONT/PRIOR F	CLASS 708	SUBCLASS 496	ART UNIT 2121	EXAMINER MAI
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APPLICANTS

Graham Kirsch

496

TITLE

Method and circuit for normalization of floating point significands in a SIMD array MPP

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
708	496	708	205				
INTERNATIONAL CLASSIFICATION							
G06F	7/38						
G06F	7/00						

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 5	Figs. Drwg. 6	Print Fig. 3	Total Claims 39	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b> 2/22/05	
	TAN V. MAI PRIMARY EXAMINER _____ (Primary Examiner) (Date) 2/2005			<b>ISSUE FEE</b> Amount Due \$1400.00 Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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